

Parameters Extraction and Modeling for Planar Spiral Inductor on Si–SiO₂ Substrates by DDM for Conformal Modules

Ji-Xiang Zhao and Jun-Fa Mao, *Senior Member, IEEE*

Abstract—In this paper, the domain decomposition method (DDM) for conformal modules is used to get simple analytic expressions for parameters of planar spiral inductors on Si–SiO₂ substrates. The conductor and substrate losses are considered in the expressions. The quality factor of the spiral inductor is computed with a transmission-line mode and compared with previously published experimental results, showing that DDM model is accurate and efficient for modeling an on-chip spiral inductor on Si–SiO₂ substrates.

Index Terms—Conformal module, domain decomposition method (DDM), planar spiral inductors, quality factor.

I. INTRODUCTION

MONOLITHIC inductors, especially in the form of spirals, have gained much application in the design of integrated RF transmitters and receivers. For this reason, the analysis and optimization of such structures have been of great importance. Considerable research work has been done over the past several years [1]–[4]. These methods are based on numerical techniques, empirical formulate, and physical models. However, the electrical parameters of spiral inductors in these methods lack a simple and accurate formula. This is a major impediment in using these methods for quick design and optimization of spiral inductors.

In this paper, the domain decomposition method (DDM) for conformal modules is used to get simple analytic expressions for parameters of planar spiral inductors on Si–SiO₂ substrates. The DDM for conformal modules can avoid complex conformal mapping. It transforms directly a planar spiral to an elongated rectangular strip. As a result, the complex problem of computing the parameters of planar spiral inductors is transformed to computing the parameters of the simple rectangular strip. By using the DDM for conformal modules, the expressions of a quality factor for a plane spiral inductor are obtained. The computed results from the expressions are compared with previously published experimental results and good agreement is achieved.

Manuscript received October 23, 2002; revised January 21, 2003. This work was supported by the National Science Foundation for Outstanding Youth of China under Grant 60025103, by the National High Technology Research and Development Program of China under Grant 2002AA1Z1520, and by the Shanghai AM Foundation under Grant 0110.

The authors are with the Department of Electronic Engineering, Shanghai Jiao Tong University, Shanghai 200030, China (e-mail: jixiang_zhao@yahoo.com; jfmao@sjtu.edu.cn).

Digital Object Identifier 10.1109/TMTT.2003.812581

II. IMPEDANCE AND QUALITY FACTOR WITH DDM

The concept of the conformal module of a quadrilateral is introduced first. Let $Q := \{\Omega; z_1, z_2, z_3, z_4\}$ denote a quadrilateral consisting of a Jordan domain Ω and four specified points z_1, z_2, z_3 , and z_4 on boundary $\partial\Omega$. The conformal module $m(Q)$ of Q is defined as follows. Suppose R_h denotes a rectangle of the form $R_h := \{(\xi, \eta) : 0 < \xi < 1, 0 < \eta < h\}$, and then $m(Q)$ is the unique value of h for which Q is conformally equivalent to the rectangular quadrilateral $\{R_h; 0, 1, 1 + ih, ih\}$. By this, we mean that, for $h = m(Q)$, and for this value only, there exists a unique conformal map $F : \Omega \rightarrow R_h$, which takes the four points z_1, z_2, z_3 , and z_4 , respectively, onto the four vertices $0, 1, 1 + ih$, and ih of R_h [5], [6].

Now consider the plane spiral inductors on a lossy Si substrate with a thin SiO₂ dielectric film between the signal conductor and Si substrate, as shown in Fig. 1(a) and (b). Both the SiO₂ film and Si substrate are uniform and infinite in the horizontal plan. The structure parameters of the spiral inductor are width w , spacing width s , thickness t_m , and the total length l . The thickness of SiO₂ and Si is t_{ox} and t_{Si} , respectively. The signal conductor is considered as a quadrilateral Q and is divided into n domains Q_i , $i = 1, \dots, n$, as described in Fig. 1(a) and (b). The crosscuts of subdivisions for the decomposition are chosen such as that in [5]. According to the DDM, the total conformal module of Q is approximated by

$$m(Q) = \sum_{i=1}^n m(Q_i). \quad (1)$$

Let T_l and A denote the trapezium and half-circular annulus sub-domain in Fig. 1(a) and (b), respectively (see Fig. 2). The conformal $m(T_l)$ and $m(A)$ are then expressed as follows [5], [7]:

$$m(T_l) = \frac{2l - w}{2w} - \frac{1}{\pi} \log 2, \quad \text{for } l \gg w \quad (2)$$

$$m(A) = \frac{\pi}{\log R - \log r}. \quad (3)$$

Subdivisions Q_2-Q_{n-1} in Fig. 1(a) are symmetrical trapeziums, which can be divided into two equal quadrilaterals like T_l in Fig. 2. Thus, by substituting the geometrical size of each subdivision in Fig. 1(a) into (2) and using (1), we get the approximated total module of the square spiral inductor, which is

$$m^s(Q) = \frac{4N(d_{\text{out}} - w)}{w} + \frac{(1 + 2N - 8N^2)(s + w)}{2w}$$

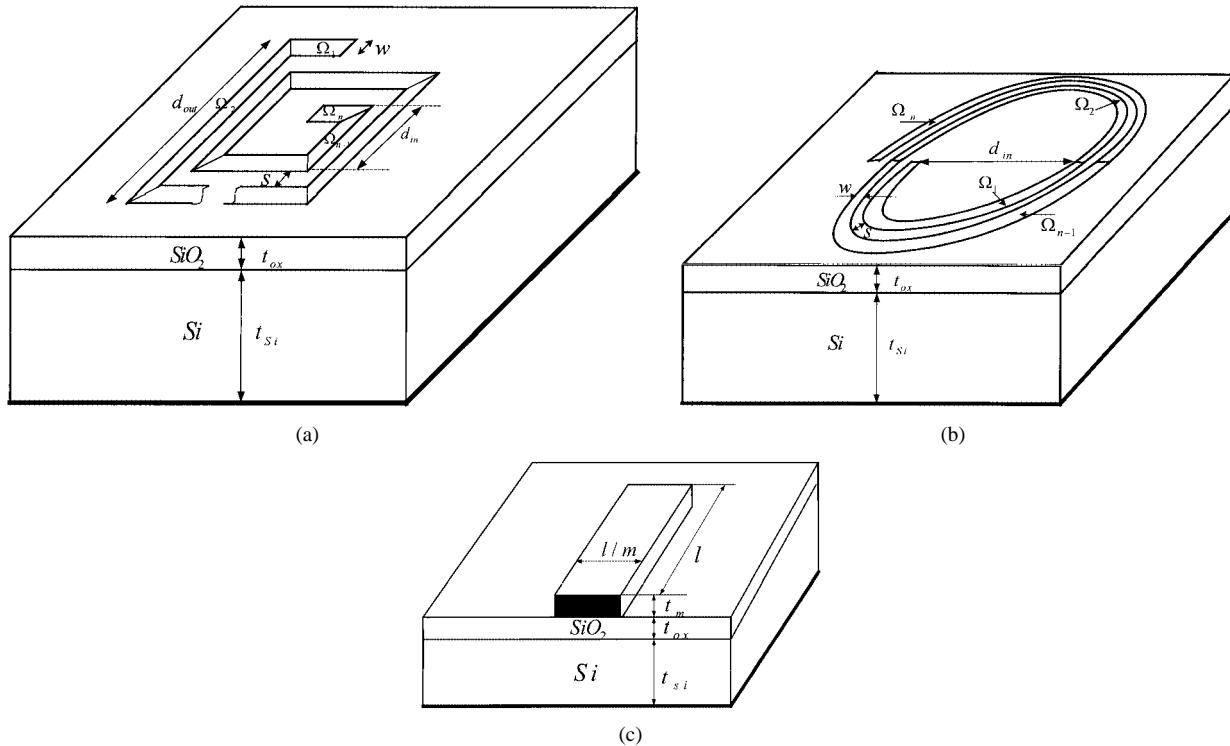


Fig. 1. On-chip inductor and its transformation by the DDM for conformal modules. (a) Square spiral inductor on Si–SiO₂ substrate. (b) Circular spiral inductor on Si–SiO₂ substrate. (c) Microstrip structure obtained from (a) and (b) by DDM for conformal modules.

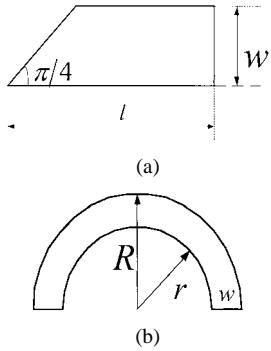


Fig. 2. (a) Trapezium sub-domain. (b) Half-circular annulus sub-domain.

$$-\frac{8N}{\pi} \log 2 \quad (4)$$

$$d_{out} = d_{in} + (2N - 1)(s + w). \quad (5)$$

Similar for the circular spiral inductor, we have

$$m^c(Q) = \pi \sum_{i=0}^{2N-1} \frac{1}{\ln \left(1 + \frac{2w}{d_{in} + i(w+s)} \right)} \quad (6)$$

where N is the turns of the inductor, d_{out} is the outer dimension, and d_{in} is the inner dimension.

The error of $m(Q)$ is [6]

$$E = 4.41 \left[\exp(-2\pi m(Q_1)) + \sum_{j=3}^n \exp(-2\pi m(Q_j)) \right]. \quad (7)$$

By the DDM, the spiral inductors in Fig. 1(a) and (b) are now transformed to the microstrip structure in Fig. 1(c). According to the method that conformal mapping extracts parameters [8], we can determine that the stripline length is l and the stripline width is $w_{eq} = l/m$. In the entire process, the parameters t_m , t_{ox} , and t_{Si} keep constant and we assume $t_{Si} \gg w$, $t_{Si} \gg t_m$, and $t_{Si} \gg t_{ox}$. Since $l \gg l/m(Q)$, we may consider the microstrip line is infinitely long. From [9], the series impedance per unit length of the microstrip is given by

$$Z = \frac{1}{\sigma_m w_{eq} t_m} + \frac{j\omega\mu}{2\pi} \ln \left(\frac{2t_{eq} + (1-j)\delta}{r_{eq}} \right) \quad (8)$$

where σ_m is the microstrip conductor conductivity, $r_{eq} = (l/m(Q) + t_m)/4$, $t_{eq} = t_{ox} + (t_m - l/m(Q))/4$, and δ is the skin depth in the lossy silicon substrate given by

$$\delta = \frac{1}{\sqrt{\pi\mu\sigma}} \quad (9)$$

where σ is the conductivity of the silicon substrate.

The characteristic impedance Z_0 of the equivalent microstrip line can be expressed from [10]

$$Z_0 = Z_{0\infty} \left(1 + \frac{j\sigma}{4\pi f \epsilon_0 \epsilon_{Si}} \right) \quad (10)$$

where

$$Z_{0\infty} = 120\pi \frac{1}{\sqrt{\epsilon_{so}^+}} \frac{(t_{ox} + t_{Si})^+}{w_e} \quad (11)$$

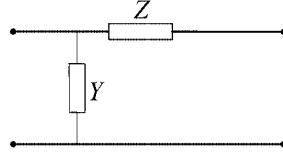


Fig. 3. Transmission-line model.

where the superscript “+” denotes effective values that take into account fringing and a mixed dielectric as follows:

$$\varepsilon_{\text{so}}^+ = \varepsilon_{\text{SiO}}^+ \frac{(t_{\text{ox}} + t_{\text{Si}})^+}{t_{\text{ox}}^+} \quad (12)$$

$$\varepsilon_{\text{SiO}}^+ = \frac{1 + \varepsilon_{\text{SiO}}}{2} - \frac{1 - \varepsilon_{\text{SiO}}}{2} \left(1 + 10 \frac{t_{\text{ox}}}{w_e} \right)^{-1/2} \quad (13)$$

$$\frac{t^+}{w_e} = \begin{cases} \frac{1}{2\pi} \ln \left[\frac{8t}{w_e} + \frac{w_e}{4t} \right], & \text{for } \frac{t}{w_e} \geq 1 \\ \left[\frac{w_e}{t} + 2.42 - 0.44 \frac{t}{w_e} + \left(1 - \frac{t}{w_e} \right)^6 \right]^{-1}, & \text{for } \frac{t}{w_e} < 1 \end{cases} \quad (14)$$

w_e is the equivalent width of the metal strip with a finite thickness, whose expression can be obtained from [11], [12] as

$$w_e = \begin{cases} w_{\text{eq}} - \sigma_m + \frac{1.25t_m}{\pi} \left[1 + \log \frac{4\pi(w_{\text{eq}} - \sigma_m)}{t_m} \right], & \text{for } \frac{w_{\text{eq}} - \sigma_m}{t_{\text{ox}} + t_{\text{Si}}} \leq \frac{1}{2\pi} \\ w_{\text{eq}} - \sigma_m + \frac{1.25t_m}{\pi} \left[1 + \log \frac{2(t_{\text{ox}} + t_{\text{Si}})}{t_m} \right], & \text{for } \frac{w_{\text{eq}} - \sigma_m}{t_{\text{ox}} + t_{\text{Si}}} \geq \frac{1}{2\pi}. \end{cases} \quad (15)$$

By using the transmission-line model depicted in Fig. 3, the shunt admittance per unit length can be obtained by

$$Y = \frac{Z}{Z_0^2}. \quad (16)$$

The admittance matrix of the two-port network in Fig. 3 is

$$[Ym] = \frac{1}{Z} \begin{bmatrix} ZY + 1 & -1 \\ -1 & 1 \end{bmatrix}. \quad (17)$$

The quality factor of the spiral inductors is then obtained as follows:

$$Q_f = \frac{\text{Im} \left(\frac{1}{Ym_{11}} \right)}{\text{Re} \left(\frac{1}{Ym_{11}} \right)}. \quad (18)$$

III. RESULTS AND DISCUSSION

The validity and accuracy of the DDM for parameters extraction of a spiral inductor are evaluated through two examples by comparison with the experimental results. For a square spiral aluminum inductor with $N = 7$ turns, width $w = 13 \mu\text{m}$, and spacing between spires $s = 13 \mu\text{m}$, outer dimension $d_{\text{out}} =$

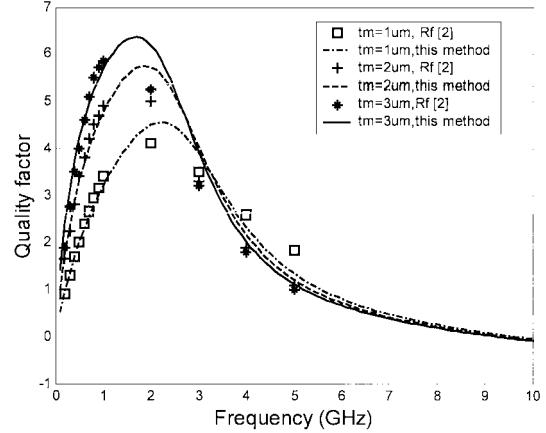


Fig. 4. Quality factor of the square inductor with different metal thickness versus frequency.

300 μm , the spiral metal of three thickness values $t_m = 1, 2, 3 \mu\text{m}$ are fabricated on a silicon substrate with resistivity $\rho = 10 \Omega\text{cm}$ and thickness $t_{\text{Si}} = 150 \mu\text{m}$. The thickness of the SiO₂ film is $t_{\text{ox}} = 4.5 \mu\text{m}$. The number of sub-domain of decomposition in the DDM is $n = 29$. The quality factor of the square spiral inductor with the three metal thickness values as a function of frequency is shown in Fig. 4 and compared with the experiment results in [2]. It is observed that there are approximately 10% deviations between the Q_f 's peak value of the DDM model and measurement. The average relative error is 1.9%, 3.4%, and 3.8% for $t_m = 1, 2$ and $3 \mu\text{m}$, respectively. As a second example, a circular spiral aluminum inductor on an Si-SiO₂ substrate with a conventional 0.6- μm CMOS three-metal-layer technology is considered. The inductors' geometrical characteristics are illuminated in the caption of Table I. The resistivity of the substrate is $\rho = 1 \sim 10 \Omega\text{cm}$. The computed results from (18) are shown in Table I and are compared with the experiment results in [13]. The computed quality factor matches the experimental results with an average relative error of 4%, 2.8%, and 5.1%, respectively.

The reason of the error includes: 1) the conformal mapping method is limited merely to quasi-static field analysis; 2) the equivalent transmission line in Fig. 1(c) is treated as of infinite length; and 3) the error of the DDM itself. According to (7), the maximum difference between the accurate total module of the square inductor and that from the DDM is

$$E = 4.41 \left[\exp(-2\pi m(Q_1)) + \sum_{j=3}^{29} \exp(-2\pi m(Q_j)) \right] = 0.0262 \quad (19)$$

which means $315.7721 \leq m(Q) \leq 315.8245$, and will lead to a relative error less than 0.01% for the quality factor. This is to say that the DDM contributes very little to the computation error of the quality factor. The main error source should be the quasi-static approximation.

It is obvious from (19) that, in order to reduce the error of the DDM, all subdivisions of a domain should be as large as possible as long as the module of each subdivision can be computed easily. For the example, for a square spiral inductor, the

TABLE I
MEASURED AND SIMULATED RESULTS FOR THE CIRCULAR INDUCTOR

Ref.	Q Vs. Frequency								Q _{max}			Average relative error[%]	
	1.272 [Ghz]		1.575 [Ghz]		1.86 [Ghz]		2.4 [Ghz]		Freq. [Ghz]	Value			
	Meas.	Sims.	Meas.	Sims.	Meas.	Sims.	Meas.	Sims.		Meas.	Sims.		
e ₁	4	4.2	4.4	4.6	5	4.9	5.6	5.4	3	5.9	5.6	4	
e ₂	4.6	4.8	5.2	5.2	5.4	5.4	5	5.4	2	5.5	5.4	2.8	
e ₃	4.9	4.8	5.1	5.2	5.2	5.4	4.5	5.2	1.9	5.3	5.4	5.1	

e₁: d_{in} = 2 × 75 μm, w = 12 μm, s = 1.5 μm, N = 3; e₂: d_{in} = 2 × 89 μm, w = 12 μm, s = 1.5 μm, N = 4;
e₃: d_{in} = 2 × 90 μm, w = 12 μm, s = 1.5 μm, N = 4.

decomposition in Fig. 1(a) in which the crosscuts are set at every corner of the spiral is the best one. More subdivisions will have no help for improving the accuracy of the DDM. The situation of the circular spiral inductor is the same.

IV. CONCLUSION

Based on the DDM for computing conformal modules, a novel transmission-line model is derived for spiral inductors on multilayer substrate. The complicated computation of mutual parameters is avoided with the DDM. The model accuracy is analyzed, showing that the error due to the DDM is very little and can be neglected. This model can be extended to an arbitrary spiral inductor as long as one can solve for the conformal module of each subdivision in the decomposition of the inductor. Due to that, closed-form expressions are available for the quality factor, and the transmission-line model is expected to be efficient for analysis and optimization of RF circuits.

REFERENCES

- W. C. Tang and Y. L. Chow, "Inductance formula of a square spiral inductor on grounded substrate by duality and synthetic asymptote," in *IEEE MTT-S Int. Microwave Symp. Dig.*, 2002, pp. 2069–2072.
- C. P. Yue and S. S. Wong, "Physical modeling of spiral inductors on silicon," *IEEE Trans. Electron Devices*, vol. 47, pp. 560–568, Mar. 2000.
- P. Pieters *et al.*, "Accurate modeling of high-Q spiral inductors in thin film multiplayer technology for wireless telecommunication applications," *IEEE Trans. Microwave Theory Tech.*, vol. 49, pp. 589–599, Apr. 2001.
- A. M. Niknejad and R. G. Meyer, "Analysis of eddy-current losses over conductive substrates with applications to monolithic inductors and transformers," *IEEE Trans. Microwave Theory Tech.*, vol. 49, pp. 166–176, Jan. 2001.
- N. Papamichael and N. S. Stylianopoulos, "On the theory and application of a domain decomposition method for computing conformal modules," *J. Comput. Appl. Math.*, vol. 50, pp. 33–50, 1994.
- , "The asymptotic behavior of conformal modules of quadrilaterals with applications to the estimation of resistance values," *Constr. Approx.*, vol. 15, pp. 109–134, 1999.
- F. Bowman, *Introduction to Elliptic Functions*. London, U.K.: English Univ. Press, 1953.
- L. H. Howell and L. N. Trefethen, "A modified Schwarz–Christoffel transformation for elongated regions," *SIAM J. Sci. Stat. Comput.*, vol. 11, no. 5, pp. 928–949, Sept. 1990.
- H. Ymeri *et al.*, "Accurate analytic expressions for frequency-dependent inductance and resistance of single on-chip interconnects on conductive silicon substrate," *Phys. Lett. A*, vol. 293, pp. 195–198, 2002.

- H. Hasegawa, M. Furukawa, and H. Yanai, "Properties of microstrip line on Si–SiO₂ system," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-19, pp. 869–881, Nov. 1971.
- H. A. Wheeler, "Transmission-line properties of strip on a dielectric sheet on a plane," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-25, pp. 631–647, Aug. 1977.
- K. C. Gupta, R. Garg, and I. J. Bahl, *Microstrip Lines and Slot-Lines*, 2nd ed. Dedham, MA: Artech House, 1996.
- J. Aguilera, J. de Nó, A. García-Alonso, F. Oehler, H. Hein, and J. Sauerer, "A guide for on-chip inductor design in a conventional CMOS process for RF applications. [Online]. Available: <http://www. amwireless. com/archives/2001/v13n10/v13n10 pg56.pdf>



Ji-Xiang Zhao received the B.S. degree in physics from Lan Zhou University, Lanzhou, Gansu, China, in 1988, the M.S. degree in microwave engineering from the University of Electronic Science and Technology of China, Chengdu, Sichuan, China, 1994, and is currently working toward the Ph.D. degree in electronic engineering at the Shanghai Jiao Tong University, Shanghai, China.

In 1988, he joined the faculty of Taiyuan University of Technology, Taiyuan, Shanxi, China, where he is currently an Associate Professor. His research is concerned with high-frequency characterization of integrated circuit devices and interconnects.



Jun-Fa Mao (M'92–SM'98) was born in 1965. He received the B.S. degree in nuclear radiation physics from the University of Science and Technology of National Defense, Changsha, China, in 1985, the M.S. degree in experimental nuclear physics from the Shanghai Institute of Nuclear Research, Academic Sinica, Shanghai, China, 1988, and the Ph.D. degree in electronic engineering from Shanghai Jiao Tong University, Shanghai, China, in 1992.

Since 1992, he has been a Faculty Member with the Department of Electronic Engineering, Shanghai Jiao Tong University. He is currently a Professor and the Associated Dean of the School of Electronic Information and Electrical Engineering, Shanghai Jiao Tong University. From May 1994 to May 1995, he was a Visiting Scholar with the Chinese University of Hong Kong. From September 1995 to August 1996, he was a Post-Doctoral Researcher with the University of California at Berkeley. His research interests include the signal integrity of high-speed integrated circuits and microwave circuits on silicon substrates. He has authored or coauthored over 60 journal papers and coauthored a book.

Dr. Mao is a Cheung Kong Scholar of Ministry of Education.